

L Number	Hits	Search Text	DB	Time stamp
-	0	cache near5 private with context	USPAT; EPO	2003/10/30 15:28
-	458	cache near5 private	USPAT; EPO	2003/10/28 15:27
-	251	cache near5 private with (processor\$1 cpu\$1)	USPAT; EPO	2003/10/28 16:20
-	294	preserv\$4 near4 context	USPAT; EPO	2003/10/28 16:39
-	3	preserv\$4 near4 context with (local\$3 private)	USPAT; EPO	2003/10/28 15:41
-	48	preserv\$4 with context with (cpu\$1 processor\$1)	USPAT; EPO	2003/10/28 15:41
-	35	preserv\$4 near4 context with (cpu\$1 processor\$1)	USPAT; EPO	2003/10/28 15:42
-	2277	((memor\$4 cache\$1) near5 (exclusive private independent)) with (processor\$1 cpu\$1)	USPAT; EPO	2003/10/28 16:21
-	28	((memor\$4 cache\$1) near5 (exclusive private independent)) with (processor\$1 cpu\$1) same context	USPAT; EPO	2003/10/28 16:22
-	0	((memor\$4 cache\$1) near5 (exclusive private independent)) with (processor\$1 cpu\$1) same context and sleep\$4	USPAT; EPO	2003/10/28 16:22
-	56	((memor\$4 cache\$1) near5 (exclusive private independent)) with (device system peripheral processor\$1 cpu\$1) same context	USPAT; EPO	2003/10/28 16:23
-	5	((memor\$4 cache\$1) near5 (exclusive private independent)) with (device system peripheral processor\$1 cpu\$1) same context and sleep	USPAT; EPO	2003/10/28 16:23
-	4659	(sav\$4 writ\$4 preserv\$4 stor\$4 maintain\$3) near4 context	USPAT; EPO	2003/10/28 16:40
-	21	(sav\$4 writ\$4 preserv\$4 stor\$4 maintain\$3) near4 context with sleep	USPAT; EPO	2003/10/28 16:48
-	156	(sav\$4 writ\$4 preserv\$4 stor\$4 maintain\$3) with context with (exclusive private independent)	USPAT; EPO	2003/10/28 16:49
-	12	(sav\$4 writ\$4 preserv\$4 stor\$4 maintain\$3) with context with (exclusive private independent) and (acpi apm sleep\$3 str)	USPAT; EPO	2003/10/28 16:52
-	253	context near2 manager\$1	USPAT; EPO	2003/10/28 16:52
-	0	context near2 manager\$1 and go6f\$.ipc.	USPAT; EPO	2003/10/28 16:52
-	203	context near2 manager\$1 and g06f\$.ipc.	USPAT; EPO	2003/10/28 16:52
-	23	context near2 manager\$1 same (processor\$1 cpu\$1) and g06f\$.ipc.	USPAT; EPO	2003/10/28 16:59
-	25	context near2 manager\$1 same (exclusive private independent)	USPAT; EPO	2003/10/28 16:56
-	182	712/228.ccls. and (exclusive private independent)	USPAT; EPO	2003/10/29 13:46
-	332	712/228.ccls.	USPAT; EPO	2003/10/29 13:46
-	157	712/228.ccls. and context	USPAT; EPO	2003/10/29 13:46
-	88	712/228.ccls. and context with (processor\$1 cpu\$1)	USPAT; EPO	2003/10/29 13:47
-	11	712/228.ccls. and context with (processor\$1 cpu\$1) same (exclusive private independent)	USPAT; EPO	2003/10/29 13:48
-	25	712/228.ccls. and context same (exclusive private independent)	USPAT; EPO	2003/10/29 13:58
-	14	(712/228.ccls. and context same (exclusive private independent)) not (712/228.ccls. and context with (processor\$1 cpu\$1) same (exclusive private independent))	USPAT; EPO	2003/10/29 13:49

-	3	712/228.ccls. and context same sleep\$3	USPAT;	2003/10/29
-	84	712/228.ccls. and context with (sav\$4 preserv\$4 writ\$4)	EPO	13:59
-	6	712/228.ccls. and context with (sav\$4 preserv\$4 writ\$4) same (exclusive private independent)	USPAT;	2003/10/29
-	3	context with (sav\$4 preserv\$4 writ\$4) with CMOS	EPO	13:59
-	0	context with (sav\$4 preserv\$4 writ\$4) with CMOS	USPAT;	2003/10/29
-	146	context with (sav\$4 preserv\$4 writ\$4)	EPO	14:00
-	98	context with (sav\$4 preserv\$4 writ\$4) and g06f\$.ipc.	USPAT;	2003/10/29
-	0	context with (sav\$4 preserv\$4 writ\$4) same private and g06f\$.ipc.	EPO	14:01
-	0	context with (sav\$4 preserv\$4 writ\$4) and private and g06f\$.ipc.	JPO;	2003/10/29
-	5	context with (sav\$4 preserv\$4 writ\$4) and (exclusive private independent) and g06f\$.ipc.	IBM_TDB	14:01
-	0	transmeta.as.	JPO;	2003/10/29
-	20	transmeta.as.	IBM_TDB	14:01
-	6	transmeta.as. and context	JPO;	2003/10/29
-	307	(name same value) same HTML	IBM_TDB	14:01
-	15	state\$1 near5 (processor\$1 cpu\$1) with crash\$4	JPO;	2003/10/29
-	15	(context state\$1) near5 (processor\$1 cpu\$1) with crash\$4	IBM_TDB	14:01
-	719	(context state\$1) near5 (processor\$1 cpu\$1) same batter\$3	JPO;	2003/10/29
-	37	(context state\$1) near5 (processor\$1 cpu\$1) same batter\$3 and 714/\$.ccls.	IBM_TDB	14:01
-	2003	(context state\$1) near5 (processor\$1 cpu\$1) same (redundant back\$1up dedicated special exclusiv\$5)	JPO;	2003/10/29
-	322	(context state\$1) near5 (processor\$1 cpu\$1) same (redundant back\$1up dedicated special exclusiv\$5) and 714/\$.ccls.	IBM_TDB	14:02
-	161	(context state\$1) near5 (processor\$1 cpu\$1) with (redundant back\$1up dedicated special exclusiv\$5) and 714/\$.ccls.	JPO;	2003/10/29
-	22	(sav\$4 preserv\$4 writ\$4) near5 (context state\$1) near5 (processor\$1 cpu\$1) with (redundant back\$1up dedicated special exclusiv\$5) and 714/\$.ccls.	IBM_TDB	14:17
-	4	(sav\$4 preserv\$4 writ\$4) near5 (context state\$1) near5 (processor\$1 cpu\$1) with (redundant back\$1up dedicated special exclusiv\$5) and 714/\$.ccls.	JPO;	2003/10/29
-	322	(context state\$1) near5 (processor\$1 cpu\$1) same (redundant back\$1up dedicated special exclusiv\$5) and 714/\$.ccls.	IBM_TDB	14:17
-	130	(context state\$1) near5 (processor\$1 cpu\$1) same (redundant back\$1up dedicated special exclusiv\$5) and 714/1?.ccls.	JPO;	2003/10/29
-	63	(context state\$1) near5 (processor\$1 cpu\$1) same (redundant back\$1up dedicated special exclusiv\$5) and 714/10,15.ccls.	IBM_TDB	14:01
-	70	(context state\$1) near5 (processor\$1 cpu\$1) same (redundant back\$1up dedicated special exclusiv\$5) and 714/2,10,15.ccls.	JPO;	2003/10/29
-	13	(batter\$3 near2 power\$3) near5 memor\$3 with (state context) and 714/\$.ccls.	IBM_TDB	14:01
-	94	(batter\$3 near2 power\$3) near5 memor\$3 with (state context)	JPO;	2003/10/29
			IBM_TDB	14:17
			USPAT	2003/10/29
			USPAT	14:17
			USPAT	2003/10/29
			USPAT	14:44
			USPAT	2003/10/30
			USPAT;	13:13
			EPO	2003/10/29
			USPAT;	15:19
			EPO	2003/10/29
			USPAT;	16:09
			EPO	2003/10/29
			USPAT;	16:09
			EPO	2003/10/29
			USPAT;	16:43
			EPO	2003/10/30
			USPAT;	09:03
			EPO	2003/10/29
			USPAT;	16:44
			EPO	2003/10/29
			USPAT;	16:52
			EPO	2003/10/29
			USPAT;	17:24
			EPO	2003/10/29
			US-PGPUB	17:24
			USPAT;	2003/10/30
			EPO	09:03
			USPAT;	2003/10/30
			EPO	09:04
			USPAT;	2003/10/30
			EPO	09:06
			USPAT;	2003/10/30
			EPO	09:16
			USPAT;	2003/10/30
			EPO	09:17
			USPAT;	2003/10/30
			EPO	09:57

-	4	(batter\$3 near2 power\$3) near5 memor\$3 with (state context) same (own separate private exclusive)	USPAT; EPO	2003/10/30 10:20
-	25	(batter\$3 near2 power\$3) near5 memor\$3 with (state context)	JPO; IBM_TDB	2003/10/30 09:58
-	23	(batter\$3 near2 power\$3) near5 memor\$3 with (own separate private exclusive)	USPAT; EPO	2003/10/30 10:28
-	12	(batter\$3 near2 power\$3) near5 memor\$3 same sleep	USPAT; EPO	2003/10/30 10:28
-	20	(shadow near2 register\$1) with (context)	USPAT; EPO	2003/10/30 13:09
-	756	shadow near2 register\$1	USPAT; EPO	2003/10/30 13:09
-	196	shadow near2 register\$1 and 712/\$.ccls.	USPAT; EPO	2003/10/30 13:10
-	32	shadow near2 register\$1 and 712/228.ccls.	USPAT; EPO	2003/10/30 13:12
-	457	shadow near2 memor\$4	USPAT; EPO	2003/10/30 13:12
-	9	shadow near2 memor\$4 and 712/228.ccls.	USPAT; EPO	2003/10/30 13:12
-	1081	(batter\$3 near3 (memor\$3 storage)) with (processor\$1 micro\$1processor\$1 cpu\$1)	USPAT	2003/10/30 14:22
-	24	(batter\$3 near3 (memor\$3 storage)) with (processor\$1 micro\$1processor\$1 cpu\$1) same (power near2 (loss failure))	USPAT	2003/10/30 14:31
-	1	4763333.pn.	USPAT	2003/10/30 14:31
-	1	sakai.in. and ACPI	USPAT; EPO	2003/10/30 15:29
-	77	4763333.URPN.	USPAT	2003/10/30 16:39
-	8	("4323987" "4327410" "4402057" "4412284" "4447887" "4458307" "4631658" "4658352").PN.	USPAT	2003/10/30 16:37
-	85	4763333.URPN. or (("4323987" "4327410" "4402057" "4412284" "4447887" "4458307" "4631658" "4658352").PN.)	USPAT	2003/10/30 16:38
-	77	(4763333.URPN. or (("4323987" "4327410" "4402057" "4412284" "4447887" "4458307" "4631658" "4658352").PN.)) and (context state)	USPAT	2003/10/30 16:38
-	34	(4763333.URPN. or (("4323987" "4327410" "4402057" "4412284" "4447887" "4458307" "4631658" "4658352").PN.)) and (context state) with batter\$4	USPAT	2003/10/30 16:38
-	1	4763333.pn.	USPAT; EPO	2003/11/03 11:07
-	956	back\$1up near5 (memory storage) with (cpu\$1 processor\$2)	USPAT; EPO	2003/11/03 11:16
-	110	back\$1up near5 (memory storage) with (cpu\$1 processor\$2) same (context state)	USPAT; EPO	2003/11/03 11:16
-	67	back\$1up near5 (memory storage) with (cpu\$1 processor\$2) same (context state) and g06f\$.ipc.	USPAT; EPO	2003/11/03 11:18
-	16	back\$1up near5 (memory storage) with (cpu\$1 processor\$2) same (context state) same (failure outage)	USPAT; EPO	2003/11/03 11:21
-	1	back\$1up near5 (memory storage) with (cpu\$1 processor\$2) same (context state) same (sleep\$4)	USPAT; EPO	2003/11/03 12:52
-	0	auxiliary near5 (memory storage) with (cpu\$1 processor\$2) same (context state) same (sleep\$4)	USPAT; EPO	2003/11/03 12:53
-	133	auxiliary near5 (memory storage) with (cpu\$1 processor\$2) same (context state)	USPAT; EPO	2003/11/03 12:53
-	78	auxiliary near5 (memory storage) with (cpu\$1 processor\$2) with (context state)	USPAT; EPO	2003/11/03 12:55
-	176	(backup secondary auxiliary) near5 (memory storage) with (cpu\$1 processor\$2) with (context state)	USPAT; EPO	2003/11/03 12:55

-	139	(backup secondary auxiliary) near5 (memory storage) with (cpu\$1 processor\$2) with (context state) and g06f\$.ipc.	USPAT; EPO	2003/11/03 12:55
-	77	(backup secondary auxiliary) near2 (memory storage) with (cpu\$1 processor\$2) with (context state) and g06f\$.ipc.	USPAT; EPO	2003/11/03 12:55
-	35	(backup secondary auxiliary) near2 (memory storage) with (cpu\$1 processor\$2) near3 (context state) and g06f\$.ipc.	USPAT; EPO	2003/11/03 12:56
-	39	(backup secondary auxiliary) near2 (memory storage) with (cpu\$1 processor\$2) near3 (context state)	USPAT; EPO	2003/11/03 12:56
-	14	5632038.URPN.	USPAT	2003/11/03 13:09
-	2506930	only	USPAT; EPO	2003/11/04 16:54
-	1259	(only near5 (cpu processor)) with (state context)	USPAT; EPO	2003/11/04 16:55
-	73	(only near5 (cpu processor)) with (state context) with (sav\$4 back\$4up)	USPAT; EPO	2003/11/04 16:55
-	253	(only near5 (cpu processor)) near5 (accessible)	USPAT; EPO	2003/11/04 16:56
-	149	(only near5 (cpu processor)) near5 (accessible) with memor\$4	USPAT; EPO	2003/11/04 16:56
-	15	(only near5 (cpu processor)) near5 (accessible) with memor\$4 same (context state)	USPAT; EPO	2003/11/04 16:59
-	6187	((access\$4) near5 only near5 (cpu processor)) with memor\$4	USPAT; EPO	2003/11/04 17:00
-	220	((access\$4) near5 only near5 (cpu processor)) with memor\$4 same (context state)	USPAT; EPO	2003/11/04 17:01
-	218	((memor\$4 near3 (access\$4)) near5 only near5 (cpu processor)) same (context state)	USPAT; EPO	2003/11/04 17:01
-	1	((memor\$4 near3 (access\$4)) near5 only near5 (cpu processor)) same ((context state) with sleep\$4)	USPAT; EPO	2003/11/05 09:24
-	291791	own	USPAT; EPO	2003/11/05 09:24
-	24	((micro\$1processor\$1 processor\$1 cpu\$1) near5 own near2 (memory storage)) with (state context)	USPAT; EPO	2003/11/05 09:50
-	80	hard adj (disk drive) with sleep	USPAT; EPO	2003/11/05 10:21
-	1	back\$3up near6 (context near2 (cpu\$1 processor\$1 micro\$1processor\$1))	USPAT; EPO	2003/11/05 10:22
-	203	(back\$3up sav\$4 stor\$4 preserv\$5) near6 (context near2 (cpu\$1 processor\$1 micro\$1processor\$1))	USPAT; EPO	2003/11/05 10:29
-	1	(back\$3up sav\$4 stor\$4 preserv\$5) near6 (context near2 (cpu\$1 processor\$1 micro\$1processor\$1)) with batter\$4	USPAT; EPO	2003/11/05 10:23
-	3	(back\$3up sav\$4 stor\$4 preserv\$5) near6 (context near2 (cpu\$1 processor\$1 micro\$1processor\$1)) same sleep\$4	USPAT; EPO	2003/11/05 10:46
-	1651	((back\$3up sav\$4 stor\$4 preserv\$5) near6 (context state)) with batter\$4	USPAT; EPO	2003/11/05 10:48
-	97	((back\$3up sav\$4 stor\$4 preserv\$5) near6 (context state)) with (own near3 (memor\$4 storage))	USPAT; EPO	2003/11/05 10:57
-	23	((back\$3up sav\$4 stor\$4 preserv\$5) near6 (context state)) with (own adj (memor\$4 storage))	USPAT; EPO	2003/11/05 10:50
-	43	((back\$3up sav\$4 stor\$4 preserv\$5) near6 (context state)) with (own near3 (memor\$4 storage))	JPO; IBM_TDB	2003/11/05 11:04
-	5	((back\$3up sav\$4 stor\$4 preserv\$5) near6 (context state)) with ((seperate private) near3 (memor\$4 storage))	JPO; IBM_TDB	2003/11/05 11:05

-	32	((back\$3up sav\$4 stor\$4 preserv\$5) near6 (context state)) with ((seperate private) near3 (memor\$4 storage))	USPAT; EPO	2003/11/05 11:06
-	166	((back\$3up sav\$4 stor\$4 preserv\$5) near6 (context state)) with ((individual seperate private) near3 (memor\$4 storage))	USPAT; EPO	2003/11/05 11:06
-	9	((back\$3up sav\$4 stor\$4 preserv\$5) near6 (context state)) with ((individual seperate private) near3 (memor\$4 storage)) same (sleep\$4 crash\$2 fail\$4)	USPAT; EPO	2003/11/05 11:09
-	14	((back\$3up sav\$4 stor\$4 preserv\$5) near6 (context state)) with ((individual seperate private) near3 (memor\$4 storage)) same (power)	USPAT; EPO	2003/11/05 11:09
-	14	((back\$3up sav\$4 stor\$4 preserv\$5) near6 (context state)) with ((individual seperate private) near3 (memor\$4 storage)) same (power\$3)	USPAT; EPO	2003/11/05 11:09
-	1	((cpu processor micro\$1processor) near5 sleep) with (context near5 (preserv\$4 sav\$4))	USPAT; EPO	2003/11/06 08:20
-	7	((cpu processor micro\$1processor) near5 sleep) with ((context state) near5 (preserv\$4 sav\$4))	USPAT; EPO	2003/11/06 08:20
-	460	(processor\$1 cpu\$1 micro\$1processor\$1) near8 (context state) with sleep	USPAT; EPO	2003/11/06 08:30
-	26	(processor\$1 cpu\$1 micro\$1processor\$1) near8 ((context state) near5 (sav\$4 preserv\$4)) with sleep	USPAT; EPO	2003/11/06 08:30
-	1	4523206.pn.	USPAT; EPO	2003/11/12 13:06
-	1	5617572.pn.	USPAT; EPO	2003/11/12 13:07
-	1	5898880.pn.	USPAT; EPO	2003/11/12 13:08